

Notice of Allowability

Application No.

10/063,456

Examiner

Derrick W. Ferris

Applicant(s)

JENKINS ET AL.

Art Unit

2616

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 7/6/2006.
2. ☒ The allowed claim(s) is/are 1-6, 8-23 renumbered as 1-22.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date 7/6/2006.
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.


DERRICK FERRIS
PATENT EXAMINER

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with William McLaughlin on 7/6/2006.

The application has been amended as follows:

In the Claims

[1 (c1)] (currently amended)

A system for providing communication between a plurality of cores in an integrated circuit, the system comprising:

a circular segmented bus operatively connected to each of the cores for transferring data between the plurality of cores; and

arbiter means for arbitrating which of the plurality of cores can transmit data at any given time, the arbiter means dynamically segmenting the circular segmented bus to enable plural simultaneous data transmissions on the bus, wherein the arbiter means is operatively connected to each of the plurality of cores and receives access requests from the cores.

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[8 (c8)] (currently amended)

The system of claim [[7]] 1 wherein the arbiter means dynamically segments the circular segmented bus responsive to the access requests.

[9 (c9)] (currently amended)

The system of claim [[7]] 1 herein the arbiter means dynamically segments the circular segmented bus responsive to the access requests and destinations for data and to provide a maximum number of simultaneous transmissions.

[10 (c10)] (currently amended)

The system of claim [[7]] 1 wherein the arbiter means dynamically segments the circular segmented bus responsive to the access requests and preselect priorities of the access requests.

[11 (c11)] (currently amended)

An integrated circuit having a reconfigurable bus comprising:

a plurality of cores;

a circular bus;

means for operatively connecting the plurality of cores around the circular bus for

transferring data on the circular bus between the plurality of cores;

isolation means operatively positioned in the circular bus between each pair of adjacent cores; and

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an arbiter operatively connected to each of the plurality of cores and to the isolation means for arbitrating which of the plurality of cores ~~ear~~es can transmit data at any given time and reconfiguring the circular bus to isolate segments of the circular bus.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Derrick W. Ferris whose telephone number is (571) 272-3123. The examiner can normally be reached on M-F 9 A.M. - 4:30 P.M. E.S.T.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wellington Chin can be reached on (571)272-3134. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.


DWF

Derrick W. Ferris
Examiner
Art Unit 2616


7/2/06
**DERRICK FERRIS
PATENT EXAMINER**